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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/676,889
		Filing Date	September 30, 2003
		First Named Inventor:	Shih-wei Liao et al.
		Art Unit	Not Yet Assigned
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	42P16806
Sheet	1	of	

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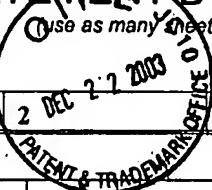
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/JM/		CAROLE DULONG, et al., "An Overview of the Intel® IA-64 Compiler," Intel Technology Journal Q4 1999, 15 pages, Intel Corporation	
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		AMIR ROTH and GURINDAR S. SOHI, "Speculative Data-Driven Multithreading," appears in the Proceedings of the 7 th International Conference on High Performance Computer Architecture (HPCA-7), Jan. 22-24, 2001, 12 pages.	
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		STEVE S. W. LIAO, et al., "Post-Pass Binary Adaptation for Software-Based Speculative Precomputation," PLDI'02, June 17-19, 2002, Berlin, Germany, pages 117-128.	
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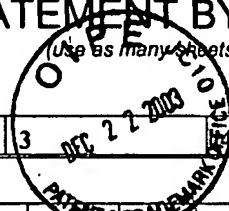
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/JM/		HING WANG ET AL: "Speculative precomputation: exploring the use of multithreading for latency" INTEL TECHNOLOGY JOURNAL, vol. 6, no.1, February 14, 2002, XP002303432, pp 1-14.	
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/JM/		YEUNG D: "Tutorial on Architecture and Compiler Support for Speculative Precomputation" TUTORIAL OUTLINE (PACT 2003), XP002348826, URL: http://maggini.eng.umd.edu/vortex/tutorial	
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/JM/		LIAO: "Post-pass binary adaptation for software-based speculative precomputation", ACM SIGPLAN NOTICES, ACM, ASSOCIATION FOR COMPUTING MACHINERY, NEW YORK, NY, US vol. 37, no. 5, May 2002, pp. 117-128, XP002302652	
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